

TITLE OF THE INVENTION

SOLID STATE SCANNING TYPE OPTICAL RECORDING DEVICE

CROSS-REFERENCE TO RELATED APPLICATIONS

5 The present application claims priority to Japanese Patent Application No. 10-326386 filed November 17, 1998, the content of which is hereby incorporated by reference.

BACKGROUND OF THE INVENTION

10 **FIELD OF THE INVENTION**

 The present invention relates to a solid state scanning type optical recording device for recording an image (latent image) on the surface of a photosensitive member by ON/OFF switching of light based on image data using a plurality of modules 15 of electro-optic elements having an electro-optic effect (e.g., an optical shutter device formed of PLZT).

DESCRIPTION OF THE RELATED ART

 In general, there have been various proposals for solid 20 state scanning type optical recording devices which control the ON/OFF switching of light for individual pixels using electro-optic elements, e.g., an optical shutter element formed of PLZT, to form an image (latent image) on an electrophotographic photosensitive member, film or a printing paper having a silver

sale-sensitive member. Heretofore, the driver integrated circuit (IC) for driving the optical shutter element has mainly controlled 64 gradients of 6-bits.

There has been demand in recent years for 8 to 12 bit 5 gradient control for digital color images. It is believed that to increase the 8 to 12 bit driver ICs henceforth will become mainstream and the number of gradients will increase.

New design and production of 8 to 12 bit driver ICs to match 10 the increase in the number of gradients present disadvantages in terms of development time and cost.

SUMMARY OF THE INVENTION

An object of the present invention is to eliminate the previously mentioned disadvantages.

15 Another object of the present invention is to provide an electro-optic element drive device which minimizes noise generation.

Another object of the present invention is to provide a 20 solid state scanning type optical recording device having few operation errors.

These and other objects are attained by an optical shutter drive device comprising:

an optical shutter module arraying a plurality of optical shutters having an electro-optic effect; and

a driver for modulating the ON time of each optical shutter element based on the image data;

wherein the driver can modulate each optical shutter element at a gradient corresponding to a predetermined number 5 of bits, so as to drive the optical shutter element at a gradient exceeding a maximum gradient at a predetermined number of bits without turning OFF the optical shutter element when driving one line.

These objects are further attained by a solid state 10 scanning type optical recording device comprising:

optical shutter elements having an electro-optic effect; and

a driver for dividing one line into a plurality of sections in the subscan direction and modulating the ON time of the optical 15 shutter elements based on the image data of each section by a predetermined number of bits fewer than the number of bits of the image data, and driving the optical shutter element at a gradient exceeding a maximum gradient corresponding to the predetermined number of bits;

20 wherein the driver includes a shift register for forwarding image data synchronously with a shift clock;

a latch register for latching image data of one section within the shift register in response to a strobe signal;

a counter for counting standard clock signals; and

a comparator for turning ON each optical shutter element, comparing the value latched in the latch register with the count value of the counter, and turning OFF each optical shutter element when both values match.

5 These objects are further attained by an electro-optic element drive device comprising:

electro-optic elements having an electro-optic effect; and

10 a driver for modulating the ON time of the electro-optic elements based on the image data by a predetermined number of bits, and driving the electro-optic element at a gradient exceeding a maximum gradient corresponding to the predetermined number of bits.

15 The invention itself, together with further objects and attendant advantages will be best understood by reference to the following detailed description taken in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

20 FIG. 1 is a brief perspective view of an optical recording device of the present invention;

FIG. 2 is a block diagram of the driver IC of the optical recording device;

FIG. 3 is a timing chart of the driver IC of a first

embodiment of the present invention;

FIG. 4 is a timing chart of the various types of signals in a first embodiment of the present invention;

FIG. 5 is a timing chart of the various types of signals 5 in the general-purpose mode of the first embodiment of the present invention;

FIG. 6 is a timing chart of the driver IC of a second embodiment of the present invention;

FIG. 7 is a timing chart of the various types of signals 10 in the second embodiment of the present invention;

FIG. 8 is a timing chart of the various types of signals in the general-purpose mode of the second embodiment of the present invention;

FIG. 9 is a timing chart of the various types of signals 15 in a third embodiment of the present invention;

FIG. 10 is a timing chart of the various types of signals in a fourth embodiment of the present invention.

In the following description, like parts are designated by like reference numbers throughout the several drawings.

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DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

The preferred embodiments of the solid state scanning type optical recording device of the present invention are described hereinafter with reference to the accompanying drawings.

Optical Recording Device (refer to FIG. 1)

FIG. 1 shows a full color optical recording device 20 application of the present invention. The optical recording device 20 records a full color image on printing paper or the 5 like, and briefly comprises halogen lamp 21, heat sink filter 22, RGB filter 25, optical fiber array 26, optical shutter module 30, and an image forming lens array 35. A full color recording is accomplished by overlaying the color-separated R, G, B images 10 of the three primary colors to reproduce the color image, and this method itself is well known.

The RGB filter 25 is driven in rotation synchronously with the recording by the optical shutter module 30 described later, so as to change the color of the light passing therethrough for each one line. The optical fiber array 26 comprises a plurality 15 of optical fibers, of which one end 26a is bundled and confronts the RGB filter 25. The other end 26b is aligned in the main scan direction represented by the X arrow in the drawing so as to emit light in a linear array.

The optical shutter module 30 provided with an array 31 20 of chips comprising a plurality of optical shutter elements formed of PLZT on a glass substrate, or slit-like apertures on a ceramic substrate, and is further provided with a driver IC 40 for each such chip. Each optical shutter element is driven by a pulse width modulation method directed only for the pixels recorded by the

driver IC 40. Polarization plates 33 and 34 are disposed anteriorly and posteriorly to the optical shutter module 30.

PLZT is a well known ceramic having light transmitting characteristics and an electro-optic effect with a large Kerr constant; the light linearly polarized by the polarization plate 33 is subjected to rotation of the polarization plane by switching ON/OFF the electric field generated by a voltage applied to the optical shutter element, so as to switch ON/OFF the light emitted from the polarization panel 34. The light emitted from the polarization plate 34 forms an image on the printing paper through the image forming lens array 35 so as to form a latent image.

In the optical recording device 20, an ND filter and a color correction filter may be disposed anterior to or posterior to the heat sink filter 22, and a slit plate may be interposed between the optical fiber array 26 and the polarization plate 33.

Driver IC (refer to FIG. 2)

FIG. 2 shows the construction of the driver IC used in the present invention. The driver IC 40 uses a plurality of n individual ICs connected by a ladder chain. Each IC 40 is constructed to drive 64 dots, and is provided with a 6-bit shift register 41, a 6-bit latch register 42, a 6-bit comparator 43, and a 6-bit counter 44 as the main components.

The image data DATA are synchronized by the shift clock signal SCLK and transmitted to the shift register 41, and latched

in the latch register 42 in response to a strobe signal STB. The gradient is set for each pixel in this way. The standard clock signals CCLK are counted by the counter 44, and the comparator 43 compares the latched value with the count value of the counter 5 44, and the moment the two values match, the control output to the optical shutter element is terminated. The counter 44 is reset by a reset signal CL. The ON/OFF switching of each optical shutter element is controlled based on the output of the comparator 43.

First Embodiment (refer to FIGS. 3, 4)

10 The first embodiment uses the driver IC 40 for driving 64 gradients at 6-bits shown in FIG. 2, so as to output images having a higher number of bits, e.g., 8-bit images (256 gradients).

15 The image data comprise a maximum of 256 gradient levels, which are divided into four 64-gradient sections, synchronized with the shift clock signal SCLK, and transmitted to the shift register 41 at time regions A1, A2, A3, A4, as shown in FIG. 3. When transmission of image data of a single cycle is completed, the strobe signal STB is set, and the counter 44 is reset. Thereafter, the shutter drive of one cycle is executed in the 20 time region B1. The shutter drive is executed successively at time regions B2, B3, B4 as the data transmission ends for each 64-gradient section.

The drive voltage normally applied to each optical shutter element is turned OFF once each time the standard clock signal

CCLK has 64 pulse transmissions in the 6-bit specification driver IC. The drive voltage is 40~70 V, and when managing 8-bit 256 gradient image data directly by a conventional 6-bit specification driver IC, the drive voltage is turned OFF with 5 a timing of a maximum four times, and the load increases on the driver IC 40.

In the first embodiment, the transmission of the standard clock signal CCLK is stopped at pulse 63, and the image data are latched and the counter 44 is reset without transmitting pulse 10 64. According to this control, the optical shutter element can be driven without unnecessarily turning OFF the drive voltage during the drive time of one line of gradients in excess of 64 gradients. In this way, the load is reduced on the driver IC 40, and noise generation is suppressed.

15 In the first embodiment, the timing is shifted for generating the strobe signal STB and the counter reset signal CL. Although both signals may be generated with the same timing, shifting the timing of the signal generation reduces the load on the driver IC 40.

20 Although the first embodiment is described by way of example representing an 8-bit (256 gradient) image via a 6-bit specification driver IC, the image may be similarly represented in gradient levels in excess of 8-bit. For example, in the case of a 10-bit (1024 gradient) image, the image data

of 64 gradients may be divided 16 times and transmitted to the shift register 41. On the other hand, it is also possible to represent even higher gradient levels via an 8-bit driver IC.
General Purpose Mode of First Embodiment (refer to FIG. 5)

5 FIG. 5 shows the general purpose mode of the first embodiment. An image of n bits ($n \geq 7$) is represented by the 6-bit specification driver IC. Of course the driver IC may have other than a 6-bit specification.

10 That is, the image data divided into 64 gradients for each pixel are transmitted to the shift register 41 at time regions $A_1, A_2, A_3 \dots A_2(n-6)$. On the other hand, the shutter drive is executed at the time regions $B_1, B_2, \dots B_2(n-6)-1, B_2(n-6)$.

Second Embodiment (refer to FIGS. 6, 7)

15 The second embodiment represents an image of higher multi-level gradients by a driver IC of fixed bit specification. Basically, the driver IC 40 of FIG. 2 is used, and turning the voltage OFF during the drive is avoided without transmitting the 64th standard clock signal CCLK to the shift register 41 similar to the first embodiment. A point of departure relative to the 20 first embodiment is that the first cycle image data are transmitted at time region A_1 , and after the data are latched in the latch register 42, the second cycle image data are successively transmitted at time region A_2 , as shown in FIGS. 6 and 7. Thereafter, the first cycle optical shutter element is

driven at the time region B1. At this time, image data are not transmitted. Directly after the first cycle optical shutter element drive ends, the data transmitted in the second cycle are latched, and subsequently the second cycle optical shutter 5 element is driven. At this time, in the time region A3, the third cycle image data are transmitted.

Voltage application to each optical shutter element starts (recording is ON) simultaneously with the start of the first cycle drive. At this time, when image data are transmitted, there is 10 concern of erroneous operation by the shift register 41 due to the influence of noise generated when recording is ON. However, in the second embodiment, the concern of erroneous operation by the shift register 41 is eliminated by avoiding image data transmission when recording is ON.

15 General Purpose Mode of Second Embodiment (refer to FIG. 8)

FIG. 8 shows the general purpose mode of the second embodiment wherein an image of n bits ($n \geq 7$) is represented by a 6-bit specification driver IC. In the general purpose mode, the driver IC naturally may have other than a 6-bit specification.

20 When the optical shutter elements are driven in the first cycle at time region B1, image data are not transmitted. Accordingly, the time region A2 ($n-6$) wherein the final gradient image data are transmitted overlaps the time region B2 ($n-6$) wherein the $2(n-6)-1$ cycle shutter drive is executed, such that

in the recording of one line, the $2(n-6)$ cycle shutter drive is executed independently at the final time region $B2(n-6)$.

Third Embodiment (refer to FIG. 9)

The third embodiment disperses the ON timing (recording 5 is ON) of each optical shutter element in addition to providing the controls described in the first and second embodiments. Specifically, as shown in FIG. 9, the standard clock signals CCLK1, CCLK2 and the like corresponding to each optical shutter element are dispersed by providing delays in the timing for transmission 10 to the comparator 43 and the counter 44. In this way, noise generation is reduced when switching the optical shutter element from the OFF state to the ON state by the small shifts in the timing of the voltage application to the optical shutter elements. The dispersion mode is not limited to that shown in FIG. 9, e.g., 15 the signals CCLK1, CCLK2 and the like may be dispersed in random sequence, or dispersed in fixed blocks.

Fourth Embodiment (refer to FIG. 10)

The fourth embodiment avoids turning OFF a plurality of optical shutter elements with identical timing so as to transmit 20 image data to the shift register 41 in addition to providing the controls of the first, second, and third embodiments. Specifically, as shown in FIG. 10, if a plurality of optical shutter elements are turned OFF with timing B' in the timing region B wherein the standard clock signal CCLK is transmitted, the shift

clock signal SCLK is not transmitted to the shift register 41 before or after so as to not transmit image data. In this way, noise generation is reduced when switching a plurality of optical shutter element from the ON state to the OFF state.

5 The timing B' can be known from the group of image data transmitted to the shift register 41 before the optical shutter element is actually driven.

10 The solid state scanning type optical recording device of the present invention is not limited to the previously described embodiments, and may be variously modified insofar as such modifications do not depart from the scope of the invention. In particular, a liquid crystal shutter, deformable device (DMD) and the like may be used instead of the PLZT as an optical shutter element.

15 In the embodiments described above, a driver IC modulates the ON time of each optical shutter element at a gradient corresponding to a fixed number of bits, and the optical shutter elements are driven at a gradient exceeding the maximum gradient of the fixed number of bits without turning OFF the optical shutter element during the drive period of one line.

20 That is, when representing an image of a greater level of gradients using a driver which represents an image of 65 gradients at a fixed 6-bits, e.g., when representing an 8-bit image of 256 gradients, in a conventional device the optical shutter are

simultaneously turned OFF at every 64th gradient, and a voltage of 40~70 V is applied to each optical shutter element. For this reason, the load on the control means increases and causes a noise generation problem. In the embodiments of the present invention, 5 however, the optical shutter elements are not turned OFF at the maximum gradient of the fixed number of bits, but rather the optical shutter elements are driven at a gradient in excess of the maximum gradient level of the fixed number of bits.

Accordingly, the optical shutter elements modulated at 10 multi-level gradient are not turned OFF simultaneously at each maximum gradient level of the fixed number of bits of the driver, thereby reducing the load on the control means, suppressing noise generation, and eliminating erroneous operation of the driver.

The most desirable method for avoiding turning OFF the 15 optical shutter element at each maximum gradient at the fixed number of bits will not transmit the comparator a drive clock signal corresponding to the maximum count value of the fixed number of bits.

If the driver is constructed so as to shift the image data 20 latch timing and the counter reset timing, the load on the driver can be reduced.

If the driver is constructed so as to avoid a timing for turning ON the optical shutter element to transmit image data to the shift register, the influence of noise can be eliminated

when switching the optical shutter element from the OFF state to the ON state when transmitting the image data. If the driver is constructed so as to turn ON each optical shutter element with a dispersed timing, noise generation can be reduced when switching

5 the optical shutter element from the OFF state to the ON state. Furthermore, if the driver is constructed so as to avoid turning OFF a plurality of optical shutter elements with identical timing to transmit image data to the shift register, noise generation can be reduced when switching the optical shutter elements from

10 the ON state to the OFF state.

Although the present invention has been fully described by way of examples with reference to the accompanying drawings, it is to be noted that various changes and modification will be apparent to those skilled in the art. Therefore, unless otherwise

15 such changes and modifications depart from the scope of the present invention, they should be construed as being included therein.